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Information Disclosure Statement By Applicant

(Use Several Sheets if Necessary)

Atty Docket No.

CISCP035/384

Applicant:

FINDLATER *et al.*

Filing Date

June 2, 1998

Application No.:

09/089,312

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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
qntw	A1	5,469434	11/21/95	Kurdzo <i>et al.</i>			01/21/93
qntw	A2	6,108,726	08/22/00	Runaldue <i>et al.</i>			0913/96
qntw	A3	5,920,698	07/06/99	Ben-Michael <i>et al.</i>			01/06/97
qntw	A4	5,943,341	08/24/99	Gazda <i>et al.</i>			10/04/96
qntw	A5	5,754,540	05/19/98	Liu <i>et al.</i>			07/18/95
qntw	A6	5,953,345	09/14/99	Findlater <i>et al.</i>			06/02/98

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	A7							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	A8	Internal Cisco documentation re: sold Ethernet switch, entitled, “CHAINSAW PCB: Internal Design Specification”, January and July 1996.
	A9	Advanced Micro Devices, “Quad Ethernet Switching Transceiver (QuEST™)”, Am79C989, Publication No. 21173, April 1997
	A10	Texas Instruments, “Eight 10Base-T Physical-Layer Interfaces”, TNETE2008, December 1997, Revised February 1998.
Examiner	<i>qntw</i>	Date Considered 5/18/01

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.